

Note: Supplied with
Nut and Washer

REVISION HISTORY			
REV	DESCRIPTION	DATE	DESIGNER
1.0	Origin	09/04/2009	P. Fayers
1.1	Insulator clips added	11/05/2009	P. Fayers
1.2	11.0 mm dim to 10.80 mm	08/06/2009	P. Fayers
1.3	EMI Flange Extended	26/06/2009	P. Fayers
1.4	PCB Layout 1.6 to 1.7 mm Hole	21/08/2009	P. Fayers
1.5	New spec sheet added	22/09/2009	P. Fayers
1.6	Panel Cut Out Added	04/11/2009	P. Fayers
1.7	Frequency range adjusted to 3.2GHz	21/07/2010	P. Fayers
1.8	Explanatory note regarding PCB layout	18/08/2010	P. Fayers
1.9	Optimised PCB for 3 GHz	31/08/2011	P. Fayers
2.0	Revised RL Graph	20/04/2012	P. Fayers
2.1	RL Graph to 6.2 GHz	03/09/2013	P. Fayers
2.2	Optimised PCB to 6.2 GHz	05/09/2013	P. Fayers
2.3	Description Chg	20/11/2018	P. Fayers

Design Right Protected Third Angle Projection	Material: DC Zinc/Phosbronze/TPX		Finish: Ni/Au/Nat		X. - +-0.5 X.X - +-0.1 X.XX - +-0.05		DO NOT SCALE	
	Designed by P. Fayers	Checked by	Approved by	Date	©2009	Date 09/04/2009	A3	
RoHS Compliant	C.E.I. Ltd		This document and all the data contained herein is and shall remain the property of Cambridge Electronic Ind. Ltd and may not be used or copied for any purpose whatsoever without the written permission or Cambridge Electronic Ind. Ltd.		6G-SDI BNC RA for PCI Express			
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D

C

B

A

Electrical:

Impedance	75 Ohms
Freq Range	0-6.0GHz
Working Voltage	500 Vrms
Dielectric withstanding voltage	1500 Vrms
Reflection Factor (VSWR)	1.16 Max DC-3.0 GHz 1.49 Max 3.0 GHz-6.0 GHz
Contact Resistance	Centre Contact 1.5 m Ohm Outer Contact 1.0 m Ohm
Insulation Resistance	> 5000 Meg Ohm

Materials:

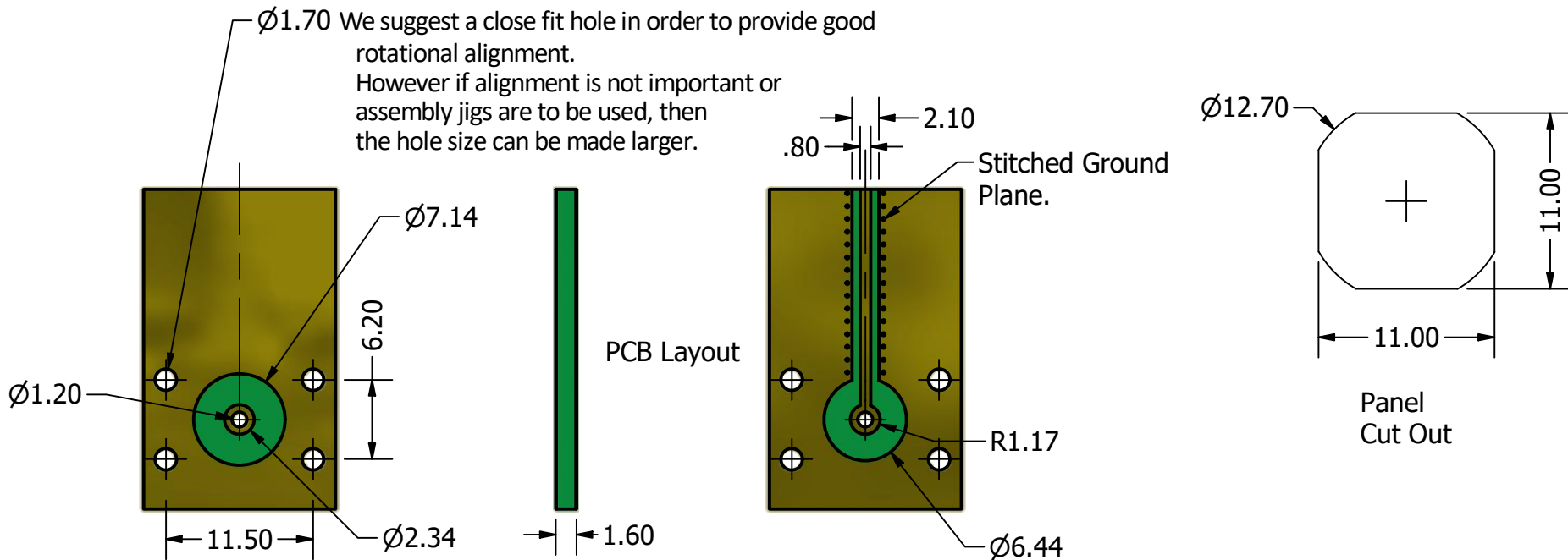
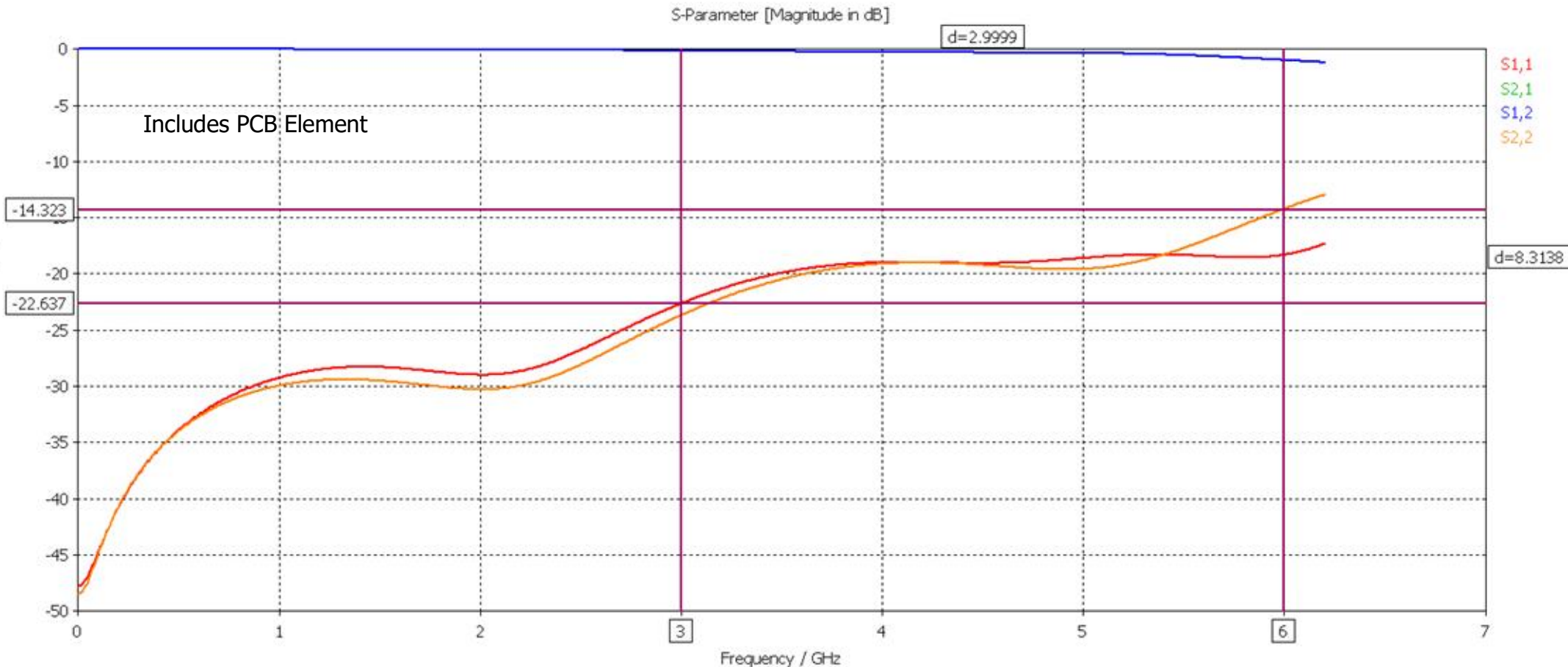
Centre Pin	Phosphor Bronze /10u" Au
Metal Parts	Die Cast Zinc/Ni
Insulator	UL94 HB TPX

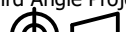
Enviromental:

Temp Range: -65 to +85°C
Mating cycles: 250
Vibration: MIL-STD-202 Method 204 test condition B
Salt Spray: MIL-STD-202 Method 101 test condition B

Processing:

Hand Solder
Wave solder capable to 265 °C
Temperatures up to 265°C may be used in Wave solder process. Dwell time 10-12 Seconds.



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