


REVISION HISTORY			
REV	DESCRIPTION	DATE	DESIGNER
1.0	Origin	07/01/2010	Peter Fayers
1.1	Electrical Spec Up Date	12/01/2012	Peter Fayers
1.2	Optimised PCB Layout Added	12/01/2012	Peter Fayers
1.3	Evaluated to 6.5 GHz	13/01/2012	Peter Fayers

Design Right Protected	Material: Brass/Be/PTFE		Finish: Au/10u Au/Nat		X.X - +-0.1 X.XX - +-0.05 X.XXX- +-0.02		DO NOT SCALE	
	Third Angle Projection 		Designed by Peter Fayers		Approved by P. Fayers			
RoHS Compliant	Cambridge Electronic Industries Ltd.		Date 03/01/2012		©2012		Date 12/01/2012	A3
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			C-SX-103					Issue 1.3

Electrical:

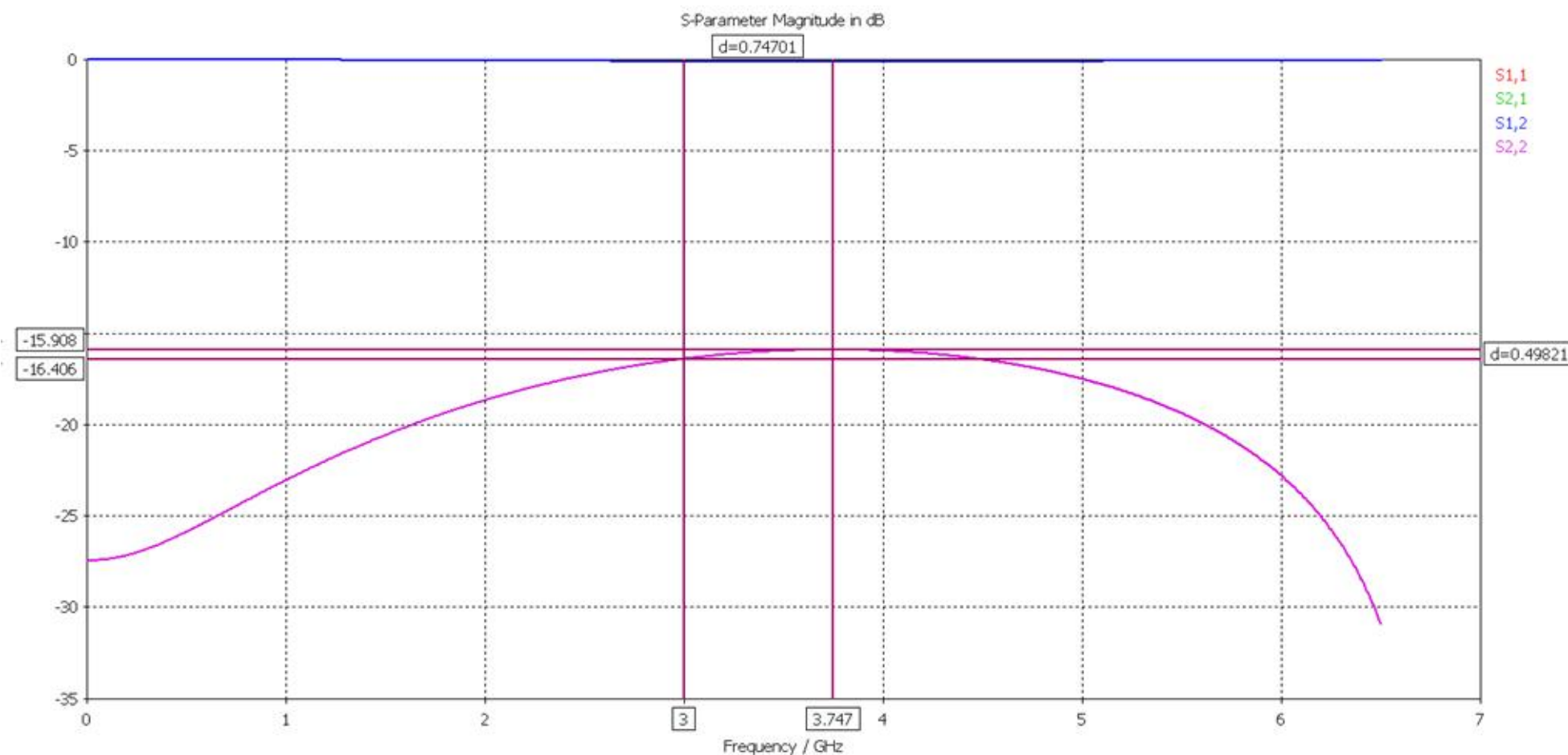
Impedance	75 Ohms
Freq Range	0-6.0GHz
Working Voltage	250 Vrms
Dielectric withstanding voltage	750 Vrms
Reflection Factor (VSWR)	1.36 Max DC-3.0 GHz 1.38 Max 3.0 GHz-6.0 GHz
Contact Resistance	Centre Contact 4.0 m Ohm Outer Contact 2.5 m Ohm
Insulation Resistance	> 1000 Meg Ohm


Materials:

Centre Pin	BeCu /10u" Au
Metal Parts	Brass/Au
Insulators	PTFE

Enviromental:

Temp Range: -65 to +85°C
Mating cycles: 500
Vibration: MIL-STD-202 Method 204 test condition B
Salt Spray: MIL-STD-202 Method 101 test condition B



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