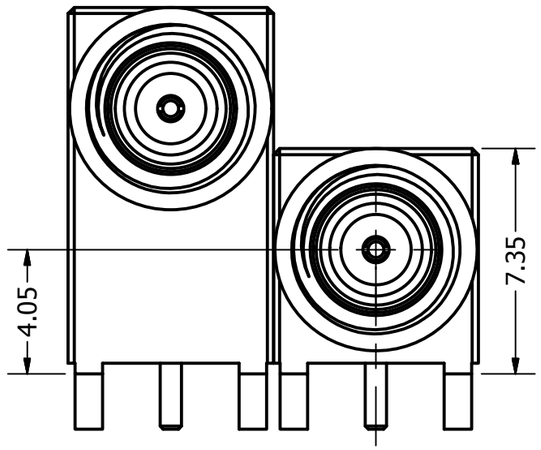
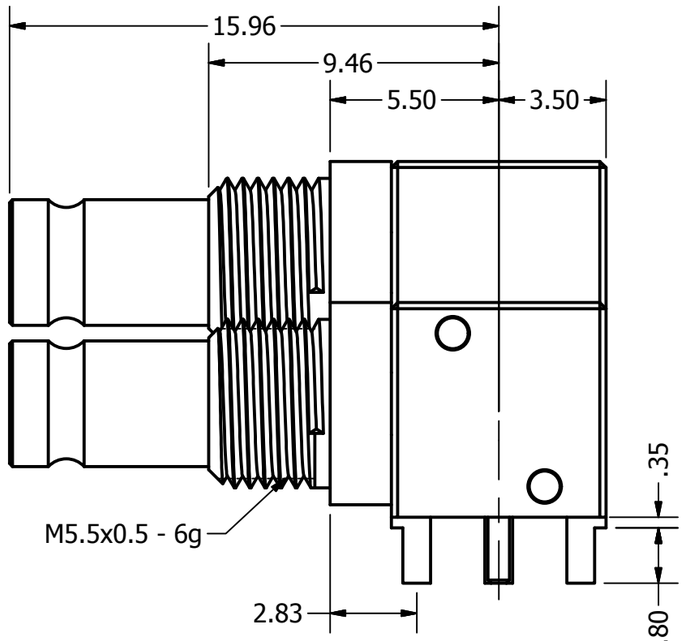
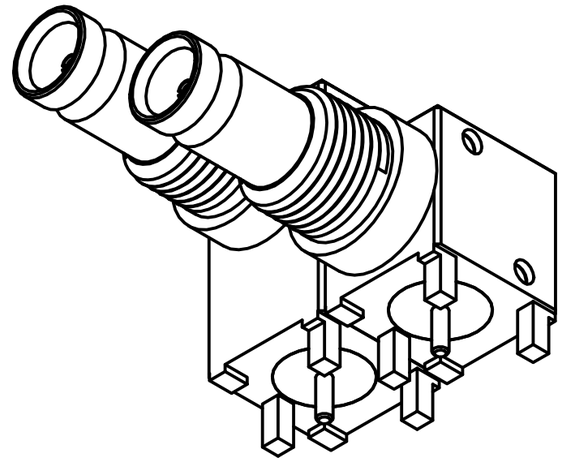
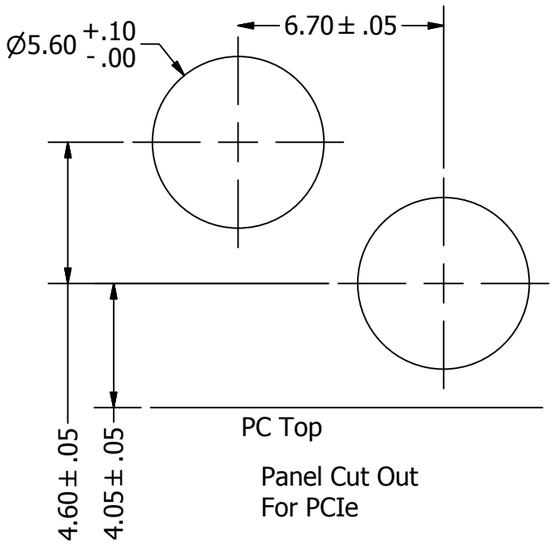
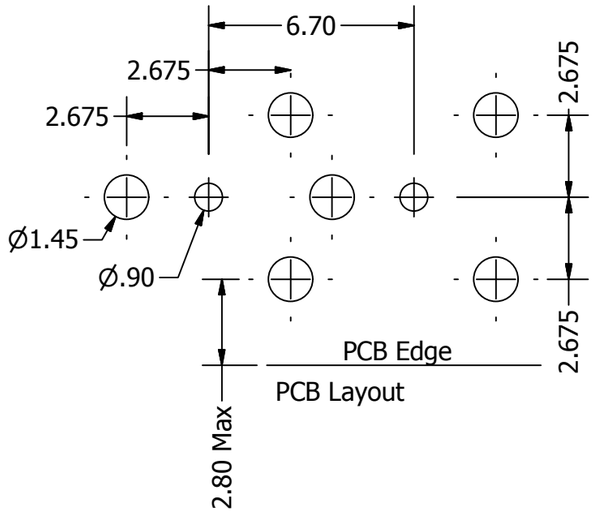
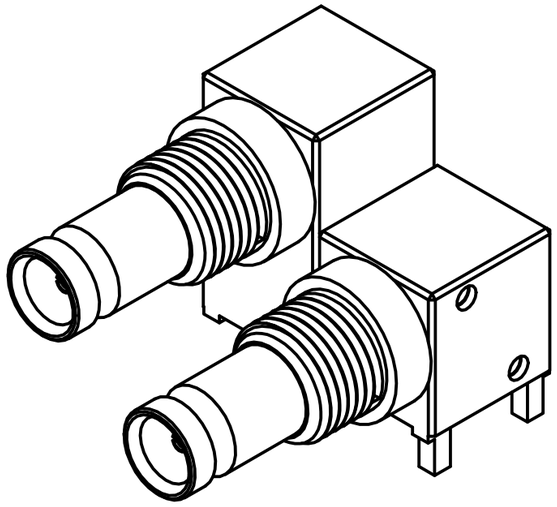


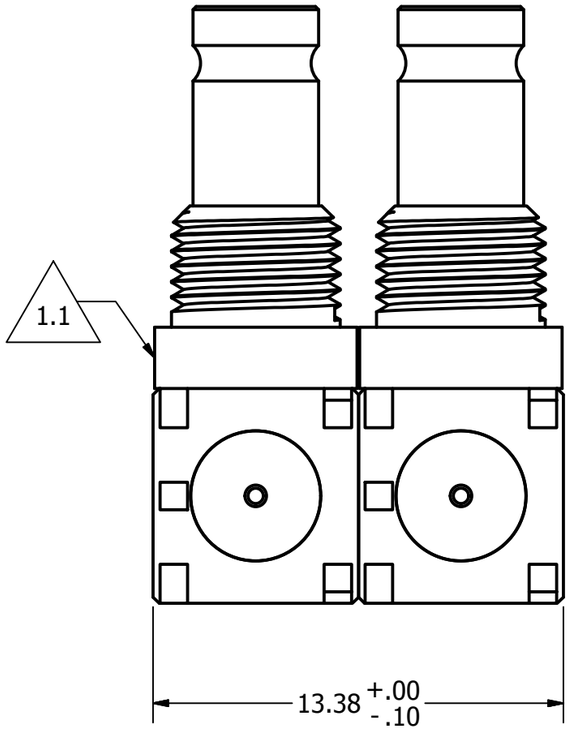


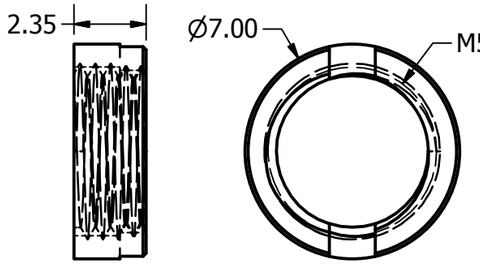








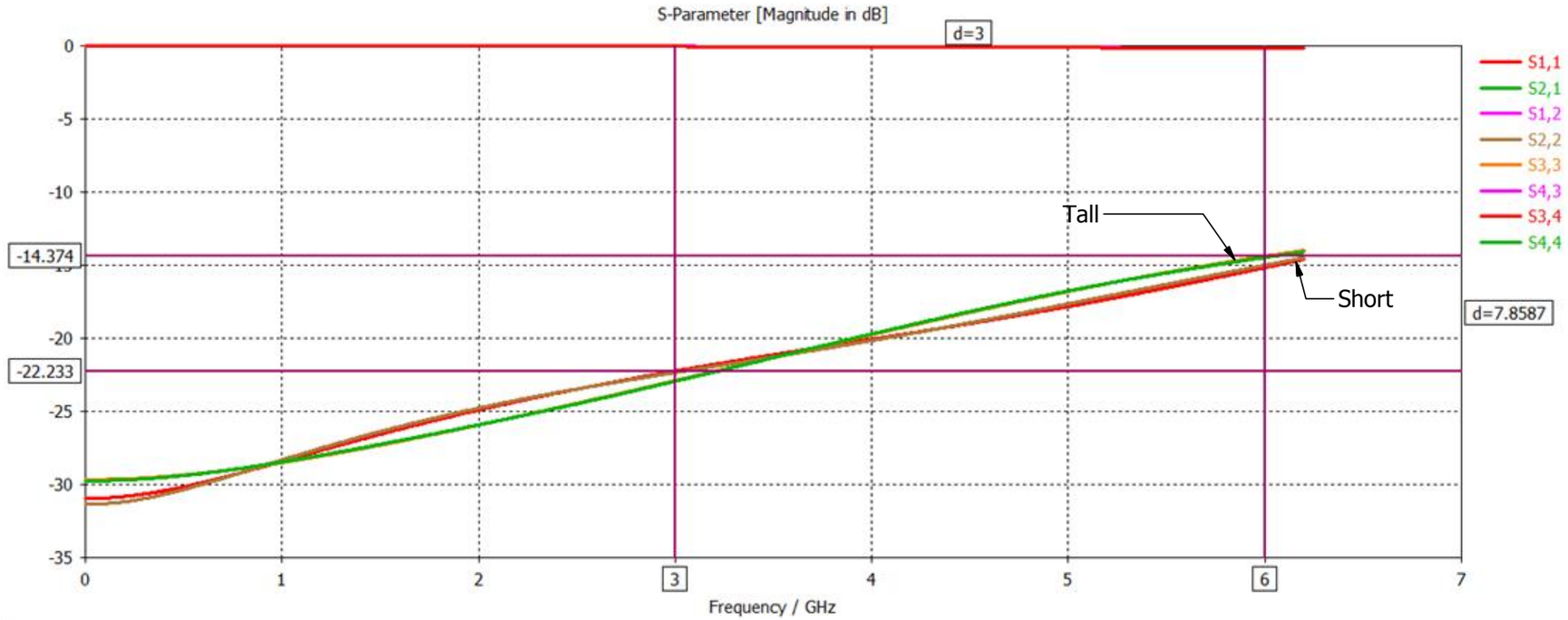




REVISION HISTORY			
REV	DESCRIPTION	DATE	DESIGNER
1.0	Origin	05/09/2012	Peter Fayers
1.1	Flange Thickness Chg	14/04/2014	Peter Fayers

Design Right Protected	Material:		Finish:	Gen Tol +/- 0.20	DO NOT SCALE	
Third Angle Projection	Designed by Peter Fayers	Checked by	Approved by Date	©2014	Unit of measure: millimetres(mm) Date 14/02/2014	A3
RoHS Compliant	**Cambridge Electronic Industries Ltd**	This document and all the data contained herein is and shall remain the property of Cambridge Electronic Industries Ltd and may not be used or copied for any purpose whatsoever without the written permission of Cambridge Electronic Industries Ltd.	2 way Dual Hight 1.0-2.3 Stacking Connector			
			C-SX-146		Issue 1.1	Sheet 1 / 2

RL No PCB Element



Electrical:

Impedance	**75 Ohms**
Freq Range	**0-6.0GHz**
Working Voltage	**250 Vrms**
Dielectric withstanding voltage	**750 Vrms**
Reflection Factor (VSWR)	**1.18 Max DC-3.0 GHz**
	1.44 Max 3.0 GHz-6.0 GHz
Contact Resistance	**Centre Contact 4.0 m Ohm**
	Outer Contact 2.5 m Ohm
Insulation Resistance	**> 1000 Meg Ohm**

Materials:

Centre Pin	**BeCu /10u" Au**
Metal Parts	**Brass/Au**
Insulators	**PTFE**

Enviromental:

Temp Range: -65 to +85°C

Mating cycles: 500

Design Right Protected	Material:		Finish:	Gen Tol +/- 0.20	DO NOT SCALE Unit of measure: millimetres(mm)
Third Angle Projection	Designed by Peter Fayers	Checked by	Approved by Date	©2014	Date 14/02/2014 A3
RoHS Compliant	**Cambridge Electronic Industries Ltd**		2 way Dual Hight 1.0-2.3 Stacking Connector C-SX-146		Issue 1.1 Sheet 2 / 2