



Recommended PCB Footprint

Recommended Panel Cut Out

REVISION HISTORY				
REV	DESCRIPTION	CHG REF	DATE	DESIGNER
3.0	Updated P/N	FB / INT	01 Aug 2024	Peter Millard
3.1	Bottom Insulators Now Common	FB / SUPP	09 Dec 2024	Peter Millard
3.2	Body now common	FB / SUPP	19 Dec 2024	Peter Millard
3.3	Updated Interface	FB / SUPP	13 Jan 2025	Peter Millard

Design Right Protected	Material:		Finish:		Gen Tol ±0.10 Angular ±2°		DO NOT SCALE		
							Unit of Measure: millimeters (mm)		
Third Angle Projection 	Designed by Peter Fayers		Checked by		Approved by		©2024	Date 18 Apr 2019	A3
RoHS Compliant	 CAMBRIDGE ELECTRONIC INDUSTRIES	This document and all the data contained herein is and shall remain the property of Cambridge Electronic Industries Ltd and may not be used or copied for any purpose whatsoever without the written permission of Cambridge Electronic Industries Ltd.			Description: 12GHz Micro BNC Dual Port Connector with enlarged flange				
					Part No: XPS-12-RB24-GNDO Cust		Issue 3.3	Sheet 1 / 2	

Electrical:

Impedance	75 Ohms
Freq Range	0-12.0 GHz
Working Voltage	250 Vrms
Dielectric withstanding voltage	750 Vrms
Reflection Factor (VSWR)	1.56 Max 0.0-12.0 GHz
Contact Resistance	Center Contact 4.0 m Ohm Outer Contact 2.5 m Ohm
Insulation Resistance	> 1000 Meg Ohm

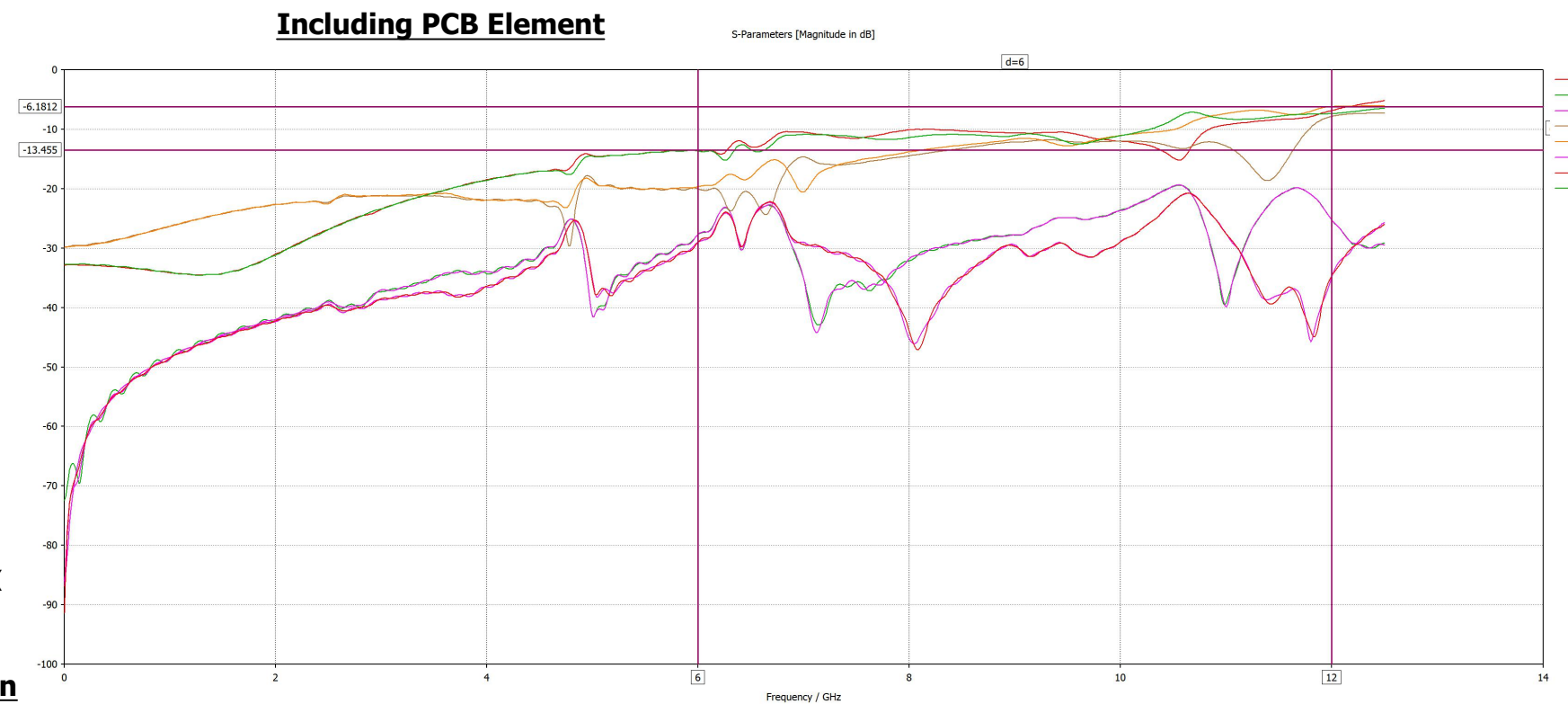
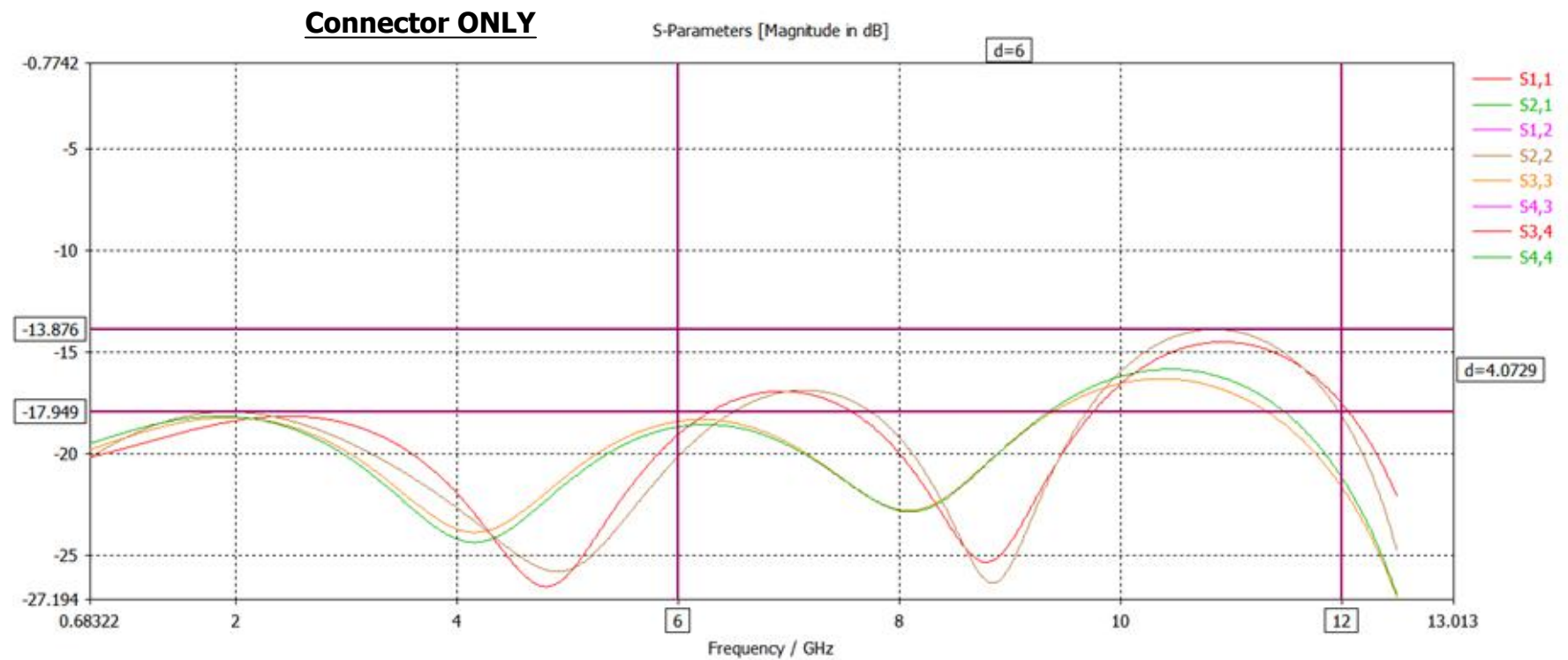
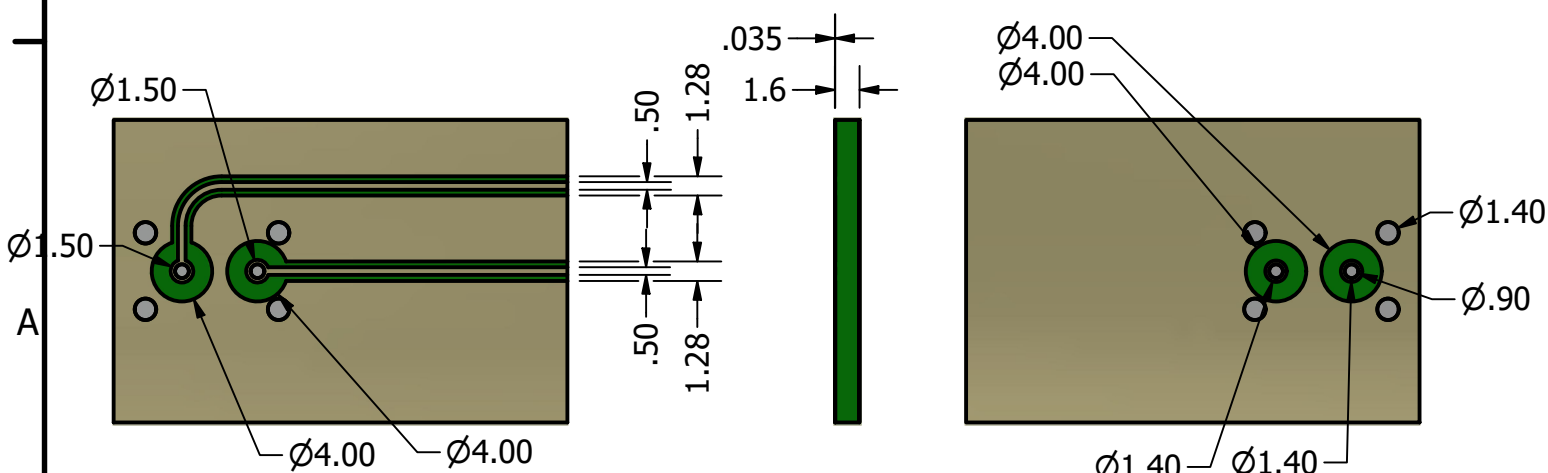
Materials:



Center Pin	BeCu/ 10μ" Au
Metal Parts	Brass / Au or Ni
Insulators	PTFE

Enviromental:

Temp Ranges:	-65 to +85°C
Mating Cycles:	500
Processing:	Hand or Wave solder Only 10-12s @260°C MAX

Suggested PCB layout intended as starting point for design iteration



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