



**Suggested PCB layout intended as starting point for design iteration**

REVISION HISTORY			
REV	DESCRIPTION	DATE	DESIGNER
3.0	Updated P/N	26 Jun 2024	Peter Millard

<b>Design Right Protected</b>	Material:		Finish:		Gen Tol ±0.10	DO NOT SCALE	
	Designed by <b>Peter Fayers</b>		Checked by		Approved by	©2024	Date 29 Jan 2015
<b>RoHS Compliant</b>	Description: 12 GHz Top Entry BNC 3 Legs		Part No: XBS-12-TE38-NNI Cust		Issue 3.0		Sheet 1 / 2
	This document and all the data contained herein is and shall remain the property of Cambridge Electronic Industries Ltd and may not be used or copied for any purpose whatsoever without the written permission of Cambridge Electronic Industries Ltd.		A3				

